



**SOCIETY FOR ELECTRONIC TRANSACTIONS AND SECURITY [SETS]**  
CIT Campus, MGR Knowledge City, Taramani, Chennai-600113, India

**Advertisement No. SETS/Chn/Rec/Proj/2024-25/28 Date: 21<sup>st</sup> October 2024**

**Society for Electronic Transactions and Security [SETS]** is a Society under the Societies Registration Act, XXI of 1860, dedicated to carry-out Research and Development in the field of Information Security focusing on the key verticals, namely, Cryptology and Computing, Hardware Security, Quantum Security and Network Security.

SETS invites applications from citizens of India to fill up the following positions for a Research & Development project in the area of Cybersecurity:

- 1. Project Scientist - 4 Positions**
- 2. Project Associates - 8 Positions**
- 3. Project Assistant - 2 Positions**

**The descriptions of positions, detailed qualification requirements and salary are given below:**

**1. Project Scientist**

<b>Name of the Post</b>	<b>Project Scientist</b>
<b>Total Number of Posts</b>	FOUR
<b>Age Limit</b>	Not more than 40 years as on 15.11.2024
<b>Essential Qualification</b>	First Class in M.Tech / M.E in relevant domain as in 1.1 or 1.2 or 1.3
<b>Experience</b>	4 - 5 Years of Relevant Experience  Equivalent work experience in lieu of higher degree: - Engineering Doctorates: Four years
<b>Remuneration</b>	Consolidated salary would be Rs.80,000/- per month, additional increment may be given based on the skill sets and relevant experience of candidate.

<b>1.1 Domain</b>	<b>Communication Post Code:24-HSRG-PS-02</b>
<b>No. of posts for the domain</b>	TWO
<b>Essential Qualification</b>	First Class M.Tech /M. E in Electronics & Communication/ Telecommunication/ Wireless Technologies or Communication/ Networking/ RF / Microwave / Communication System
<b>Desirable Qualification</b>	PhD in Electronics & Communication/ Telecommunication/ Wireless Technologies or Communication/ Networking/ RF / Microwave / Communication Systems
<b>Areas of Skill sets/ Knowledge required</b>	a) Knowledge of Wireless Protocols/ Sensor Networks/ TCP/IP suite/ Network Security / Antenna Theory  b) Knowledge of RF analysis with GNU Radio, Spectrum Analysers, Software-Defined Radios (SDR) and Anechoic chambers  c) Knowledge of C/C++ / Python programming and to work with GNU Radio/ MATLAB / Wireshark / tcpdump/ SCAPY in Ubuntu/ Kali Linux / Windows environment

	<p>d) Candidates with Wireless Penetration Testing or Ethical hacking Certificates/ Experience are preferred.</p> <p>e) Hands-on Experience with network scanning tools (e.g., Nessus, Nmap) and penetration testing tools (e.g., Metasploit).</p> <p>f) Candidates with Penetration Testing or Ethical hacking Certificates/ Experience are preferred.</p>
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<b>1.2 Domain</b>	<b>Software Post Code:24-HSRG-PS-03</b>
<b>No. of posts for the domain</b>	ONE
<b>Essential Qualification</b>	First Class M. Tech /M. E in Electronics & Communication/ Computer Science/ Information Technology / Information and Communication Technology / Cyber Security / Communication System
<b>Desirable Qualification</b>	PhD in Electronics & Communication/ Computer Science/ Information Technology / Information and Communication Technology / Cyber Security / Communication System
<b>Areas of Skill sets/ Knowledge required</b>	<p>a) Programming Experience in C/C++, Python, Linux.</p> <p>b) Strong understanding of embedded firmware, reverse engineering techniques, and hands-on experience working with binary analysis tools (E.g. Ghidra, IDA Pro).</p> <p>c) Hands-on experience in Firmware Emulation tools, binary debugging tools.</p> <p>d) Hands-on Experience in BareMetal firmware.</p> <p>e) Understanding of common binary protection techniques, such as code obfuscation, and anti-debugging.</p> <p>f) Familiarity with x86, ARM, MIPS, and other embedded system architectures.</p>

<b>1.3 Domain</b>	<b>Hardware Post Code:24-HSRG-PS-04</b>
<b>No. of posts for the domain</b>	ONE
<b>Essential Qualification</b>	First Class M. Tech /M. E in Electronics & Communication/ Communication Systems /Applied Electronics/VLSI Design /Embedded Systems
<b>Desirable Qualification</b>	PhD in Electronics & Communication/ Communication Systems /Applied Electronics/VLSI Design /Embedded Systems
<b>Areas of Skill sets/ Knowledge required</b>	<p>a) Knowledge of hardware components like Processors, Memory, Peripherals and storage devices</p> <p>b) Knowledge of various communication protocols for interfacing between devices</p> <p>c) Knowledge of circuits and PCB Reverse Engineering.</p> <p>d) Hands-on experience in Hardware debugging tools</p> <p>e) Knowledge of Linux and Windows Operating Systems</p>

## 2. Project Associate-II & Project Associate-I

<b>Name of the Post</b>	<b>Project Associate-II &amp; Project Associate-I</b>
<b>Number of Posts</b>	FOUR each
<b>Age Limit</b>	Not more than 35 years as on 15.11.2024
<b>Essential Qualification</b>	First Class M.Tech /M. E in the relevant domain as in 2.1, 2.2 or 2.3
<b>Experience</b>	2 Years of Relevant Experience will be considered as Project Associate-II
<b>Remuneration</b>	Consolidated salary would be Rs. 55,000/- to 65,000/- per month, additional increment may be given based on the skill sets and relevant experience of candidate.

<b>2.1 Domain</b>	<b>Communication Post Code:24-HSRG-PA-04</b>
<b>No. of posts for the domain</b>	FOUR
<b>Essential Qualification</b>	First Class M. Tech /M. E in Electronics & Communication/ Telecommunication/ Wireless Technologies or Communication / Networking/ RF / Microwave / Communication System
<b>Areas of Skill sets/ Knowledge required</b>	<p>a) Knowledge of Wireless Protocols/ Sensor Networks/ TCP/IP suite/ Network Security / Antenna Theory</p> <p>b) Knowledge of RF analysis with GNU Radio, Spectrum Analysers, Software-Defined Radios (SDR) and Anechoic chambers</p> <p>c) Knowledge of C/C++ / Python programming and to work with GNU Radio/ MATLAB / Wireshark / tcpdump/ SCAPY in Ubuntu/ Kali Linux / Windows environment</p> <p>d) Candidates with Wireless Penetration Testing or Ethical hacking Certificates/ Experience are preferred.</p> <p>e) Hands-on Experience with network scanning tools (e.g., Nessus, Nmap) and penetration testing tools (e.g., Metasploit).</p> <p>f) Candidates with Penetration Testing or Ethical hacking Certificates/ Experience are preferred.</p>

<b>2.2 Domain</b>	<b>Software Post Code:24-HSRG-PA-05</b>
<b>No. of posts for the domain</b>	TWO
<b>Essential Qualification</b>	First Class M. Tech /M. E in Electronics & Communication/ Computer Science/ Information Technology / ICT/ Communication System
<b>Areas of Skill sets/ Knowledge required</b>	<p>a) Programming Experience in C/C++ and Python.</p> <p>b) Knowledge of Linux Operating System.</p> <p>c) Hands-on Experience in Penetration Testing Tools, Web Application Security and Linux Hardening.</p> <p>d) Knowledge on RTOS based firmware.</p> <p>e) Experience in Exploit Development</p> <p>f) Hands-on Experience in code reverse engineering.</p> <p>g) Hands-on experience working with binary analysis tools (E.g. Ghidra, IDA Pro).</p> <p>h) Practical experience with Assembly language for ARM, MIPS and x86 architectures.</p>

<b>2.3 Domain</b>	<b>Hardware Post Code:24-HSRG-PA-06</b>
<b>No. of posts for the domain</b>	TWO
<b>Essential Qualification</b>	First Class M. Tech /M. E in Electronics & Communication/ Communication Systems /Applied Electronics/VLSI Design /Embedded Systems
<b>Areas of Skill sets/ Knowledge required</b>	<ul style="list-style-type: none"> <li>a) Knowledge of hardware components like Processors, Memory, Peripherals, and storage devices</li> <li>b) Knowledge of various communication protocols for interfacing between devices</li> <li>c) Knowledge of circuits and PCB Reverse Engineering.</li> <li>d) Hands-on experience in Hardware debugging tools</li> <li>e) Knowledge of Linux and Windows Operating Systems</li> </ul>

### 3. Project Assistant

<b>Name of the Post</b>	<b>Project Assistant Post Code:24-HSRG-PAsst-01</b>
<b>Number of Posts</b>	TWO
<b>Age Limit</b>	Not more than 28 years as on 15.11.2024
<b>Essential Qualification</b>	First Class B.E / B.Tech ECE/EEE/E&I/CSE/IT/ICT
<b>Areas of Skill sets/ Knowledge required</b>	<ul style="list-style-type: none"> <li>a) Programming knowledge in C/C++ and Python</li> <li>b) Knowledge of Windows and Linux environments</li> <li>c) Good skill in the documentation and presentation preparation of Technical Reports using tools like Word, Excel, Latex, Power Point etc.</li> </ul>
<b>Remuneration</b>	Consolidated salary would be Rs.30,000/- per month, additional increment may be given based on the skill sets and relevant experience of candidate.

The positions of **Project Scientist, Project Associate-II & I and Project Assistant** as proposed is purely temporary and would be filled on a contract basis with a consolidated salary under project mode. The duration of the assignment is initially for a period of One Year and would be extended further based on the need of the project and performance. There is no scope of continuation / regularization/ absorption under any circumstances.

### Application Procedure:

1. Only applications received via Google Form will be considered. The Candidate should choose the appropriate post to apply (“Project Scientist, Project Associate-II & I and Project Assistant”). Please find below the link for the Google form:

Name of the Post	Domain	Post Code	Google Form Link
1. Project Scientist	1.1 Communication	24-HSRG-PS-02	<a href="https://forms.gle/6YqpuvjQwSh3EBYG9">https://forms.gle/6YqpuvjQwSh3EBYG9</a>
	1.2 Software	24-HSRG-PS-03	
	1.3 Hardware	24-HSRG-PS-04	
2. Project Associate – II & I	2.1 Communication	24-HSRG-PA-04	<a href="https://forms.gle/LFNEZ7dnopmz988WA">https://forms.gle/LFNEZ7dnopmz988WA</a>
	2.2 Software	24-HSRG-PA-05	
	2.3 Hardware	24-HSRG-PA-06	
3. Project Assistant	-	24-HSRG-PAsst-01	<a href="https://forms.gle/cGYQkouZx6MBFTYRA">https://forms.gle/cGYQkouZx6MBFTYRA</a>

2. The candidate is requested to attach the following documents:-
  - a) Scanned copy of Personal Particulars form (pdf format) duly filled and signed. The Personal Particulars form is available at SETS home page <https://setsindia.in/careers> .
  - b) Scanned copies of academic certificates (Single PDF document)
  - c) Scanned copies of Experience certificates (Single PDF document)
3. The last date for uploading applications via Google Form is 15.11.2024 before 6 PM.
4. Shortlisted candidates would be required to attend a Written Test and / or Interview at SETS, Chennai. The date and time for this would be intimated to shortlisted candidates by E-mail.

### Terms and Conditions:

1. The shortlisted candidates would be required to bring all their original testimonials for verification on the interview day.
2. No TA/DA will be given to candidates appearing for interview.
3. The prescribed qualifications are minimum and mere possession of the same does not entitle the candidate to be called for the written test or interview. The decision of the Executive Director of SETS in all matters relating to eligibility, acceptance or rejection of the applications, cancellation of advertisement and filling up or not filling up of post will be final and no inquiry or correspondence will be entertained in this matter.
4. Number of vacancies may increase/decrease depending upon SETS requirements and such changes will be made by SETS without any notice.

**Executive Director**